



**SUPERSEDING TOLERANCES**  
 .XXX ±.01  
 .XX ±.03

DC PIN	NON TTL	TTL
1	GND	+Vdc
2	+Vdc	GND
3	NA	CONT.

CONTROL	DIM. A
NON TTL	1.40 MAX.
TTL	1.80 MAX.

REV	ECN	DATE	BY	APPR
-	ISSUED	6-11-04	BZ	TEZ
A	11738	8-22-05	BZ	TEZ
B	17103	4-20-09	TEZ	BAC
C	18839	7-7-10	JZ	TEZ
D	23000	7-19-13	JZ	TEZ
E	23702	1-29-14	TEZ	TEZ
F	24500	5-28-14	TEZ	TEZ



**JFW INDUSTRIES, INC.**

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MATERIAL	PROPRIETARY INFORMATION	
FINISH	ALL SURFACES ARE TO BE FREE OF BURRS, PITS & SCRATCHES	
MODEL	50S-XXXX	SCALE FULL
DESCRIPTION	1P2T FAILSAFE RF SWITCH	TOLERANCES .XX±.010 .XXX±.005 XX°±.5°
		DWG # 092-4496